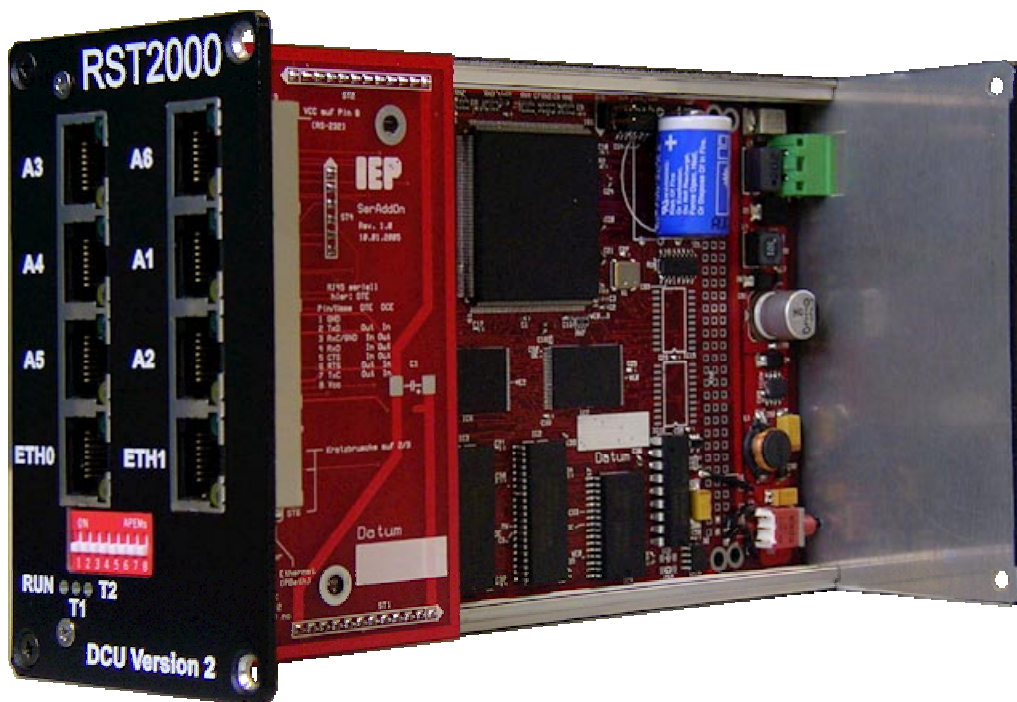


PBeth2

Communications controller



4 fast serial RS-232-interfaces for synchronous and asynchronous protocols are the main feature of the **PBeth2**. The MC68360's communications controller supports direct use of each interface as well as time-division multiplexing.

Based on the MC68360, the **PBeth** is a single board computer for stand-alone deployment or for use as CPU in PBus systems.

Outstanding features of the **PBeth2** are:

- 4 independent serial interfaces up to 10 MBaud (SCC)
- Low level protocol handling provided by an independent, microprogrammed communications controller
- 2 RJ45 network interfaces
- 2 asynchronous serial RS-232-ports

PBeth2

Communicative

Protocols

The standard microcode of the MC68360 handles supports numerous synchronous and asynchronous protocols, e.g. UART, HDLC and transparent operation.

The **PBETH2** allows access to the SCC's by 4 serial RS-232 interfaces, supporting also receive- and transmit clocks aside from the regular data and handshake lines. Interface converter for RS-422/RS-485, also supporting synchronous protocols, are available.

The range of supported protocols can be broadened by a loadable microcode memory. Microcode libraries, supporting e.g. Profibus or LAPB, are available.

Ethernet

The **PBeth2** features 2 independent RJ45-Ethernet interfaces. The PBeth2 can simultaneously connect to an IT- and a control network without compromising the control systems integrity.

A TCP/IP stack is in the standard scope of supply. An optional OSI stack serves for the integration of PBus systems e.g. into control concepts based on the Siemens-SINEC-H1 protocol.

Management

Two more asynchronous RS-232 interfaces are available as e.g. programming or operating interfaces or for the connection of additional peripherals, e.g. of a GPS receiver. With their direct interrupt handling, the handshake inputs can be used to give an exact time trigger on GPS time pulses.

Size and Supply

The **PBeth2** is available as 24 V_{DC} stand-alone system in a closed casing for DIN-rail-mounting or as plug-in card for PBus-systems with a supply of 5 V_{DC}.

With 4 MB DRAM and 4 MB Flash as well as 512 kB battery buffered SRAM and an RTC the **PBeth2** provides enough resources even for demanding applications.

The realtime operating system RTOS-UH with runtime support for programming in ANSI-C or PEARL is in the standard scope of supply, runtime support for IEC 61131-3 programming with the CoDeSys programming system is available.

Order reference

